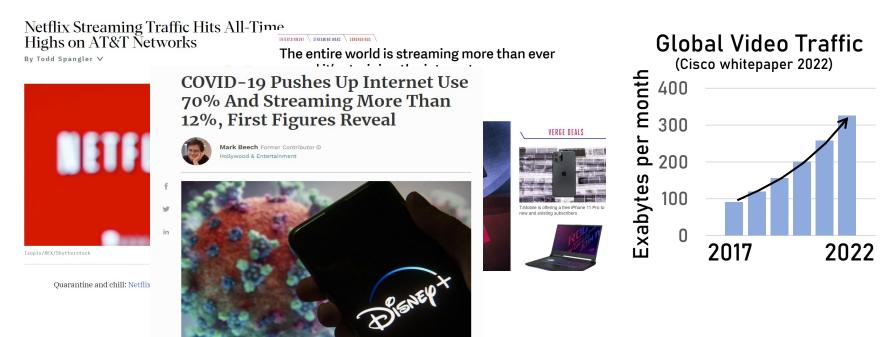


Rearchitecting the TCP Stack for I/O-Offloaded Content Delivery

Taehyun Kim, Deondre Martin Ng, Junzhi Gong*, Youngjin Kwon, Minlan Yu*, KyoungSoo Park KAIST & *Harvard University

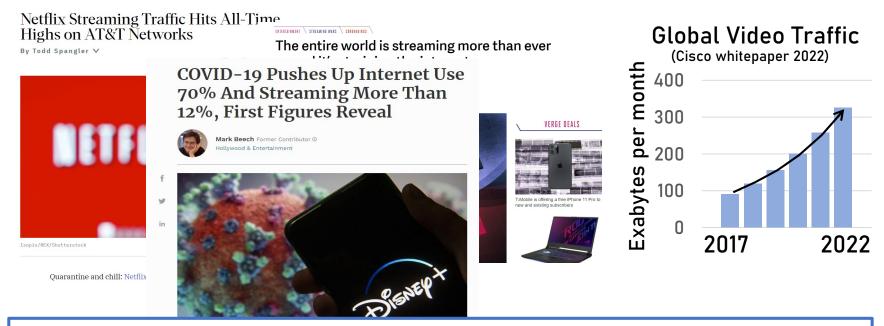
Increasing Demand for High-quality Video Streaming

COVID-19 pandemic (2020~) – "more" rapid increase in video traffic

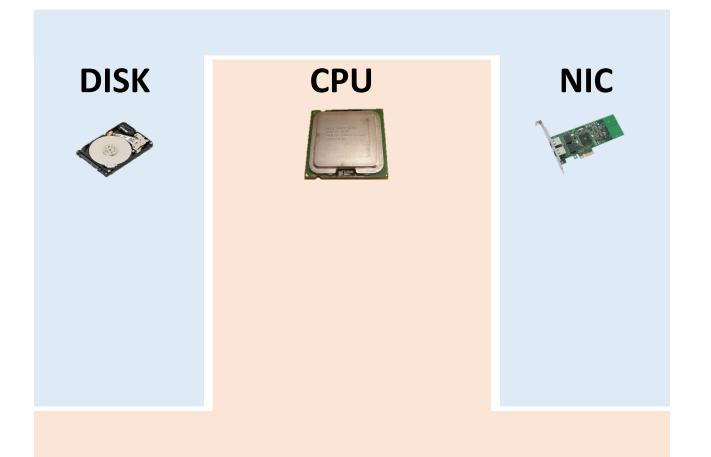


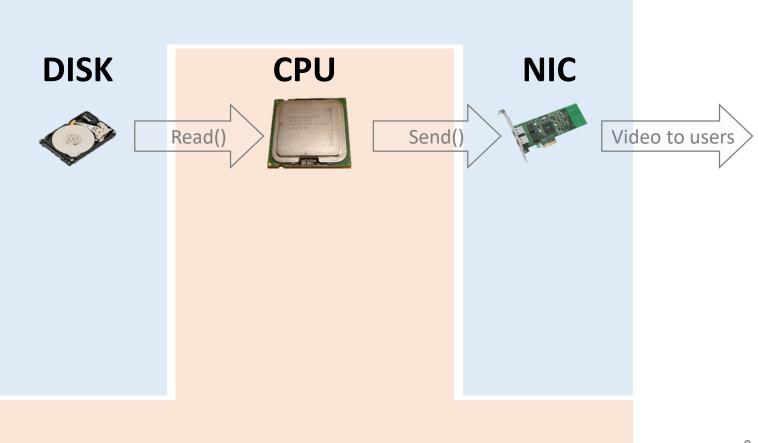
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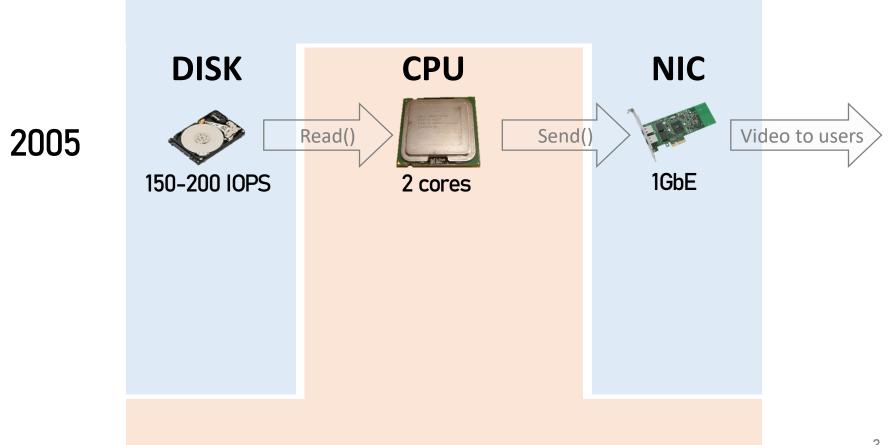
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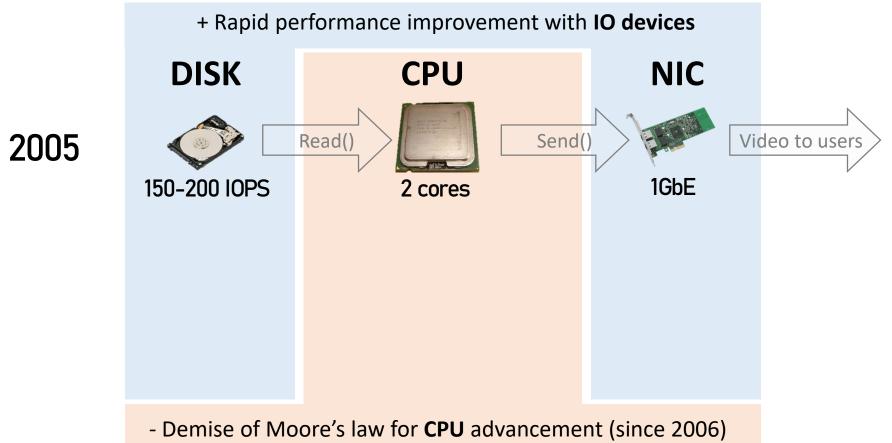


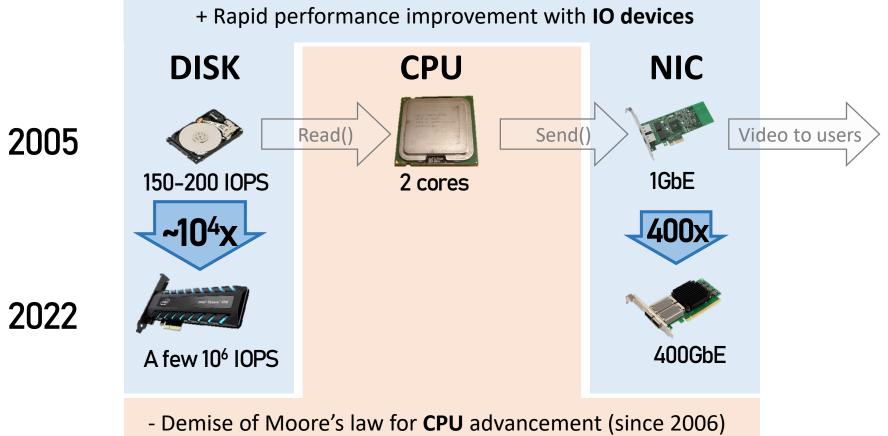
CDN server performance is critical for cost-effective service

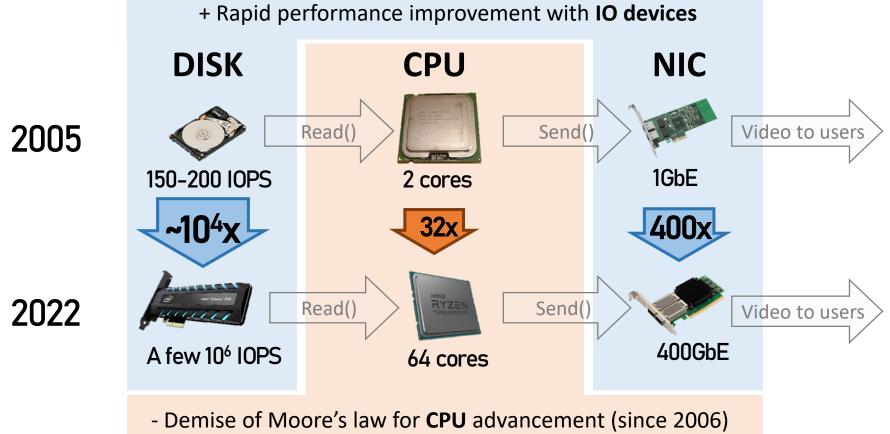












- Typical server operations
 - Read an HTTP request
 - Read a file chunk for the request
 - Send the response
- Result

- nginx (v.1.80.0) on Linux
- 300KB files on 4x Optane NVMe
- 100Gbps NIC
- Single core of Xeon Silver 4210

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Data plane: 72%		Control plane: 28%	
Disk IO	Memory Management	Network IO open()	Application TCP control logic

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Data plane: 72% Control plane: 28% Disk I0 Memory Management Network I0 open() fstat() Application TCP control logic

Why CPU bottleneck for IO-bound workload?

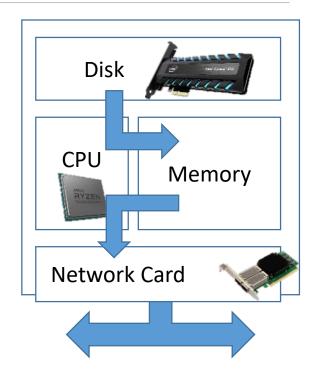
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Root cause: "CPU-centric" OS Abstractions

- Modern OS designed with "implicit" assumptions
 - CPU is fast but IO devices are slow
 - CPU is never bottleneck for IO-bound workloads

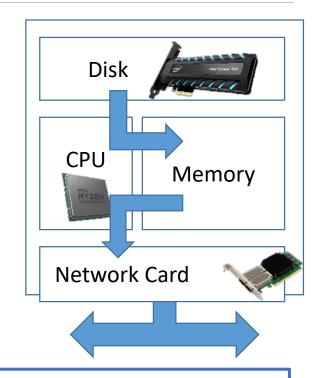
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 - All content must be brought to "main" memory first!
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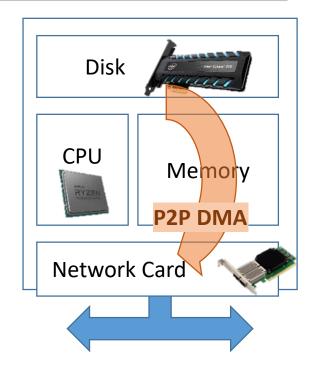
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How to avoid CPU bottleneck for IO-bound workload?

Opportunity in the Solution Space

- Modern PCIe devices support P2PDMA
 - Peer-to-peer DMA without CPU intervention
 - No main memory copy if the DMA devices have memory
- **Programmability** in IO devices
 - SmartNICs & computational SSDs
 - Arm SOC, FPGA-based, or ASIC-based
- Approach: SmartNIC as the hub for NVMe disk IOs



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Disk

Network Card

CPU

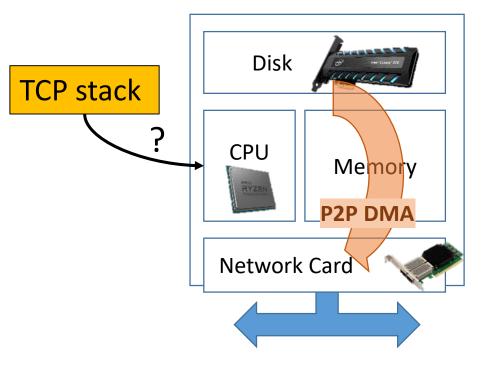
anne

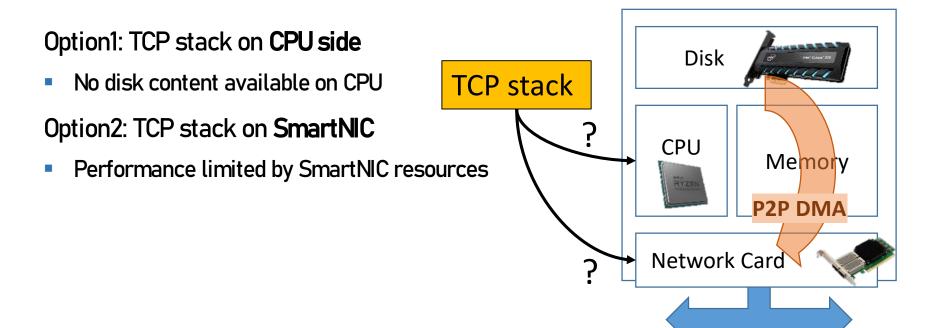
Memory

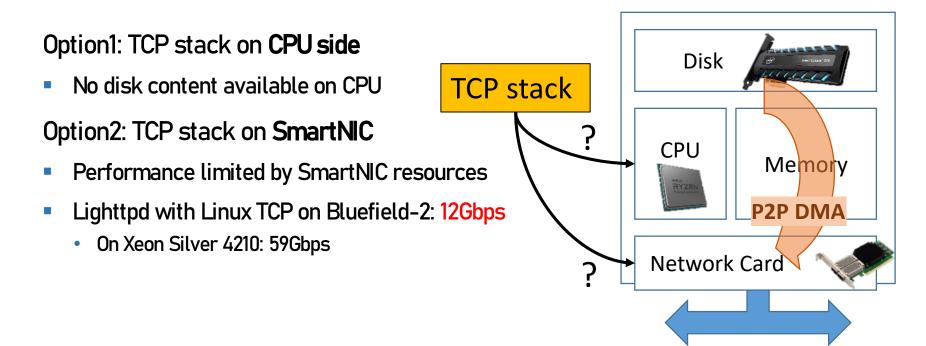
P2P D

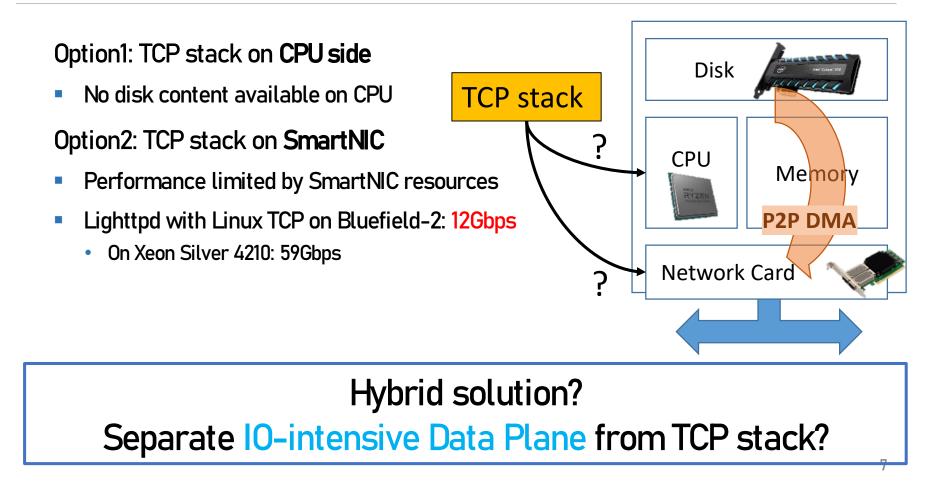
Option1: TCP stack on CPU side

No disk content available on CPU



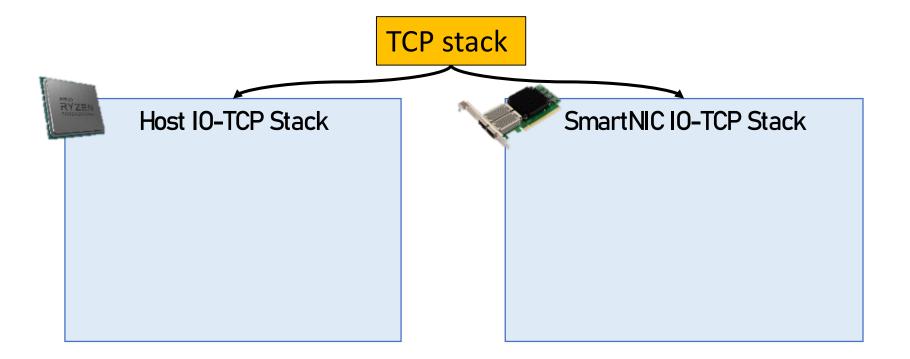






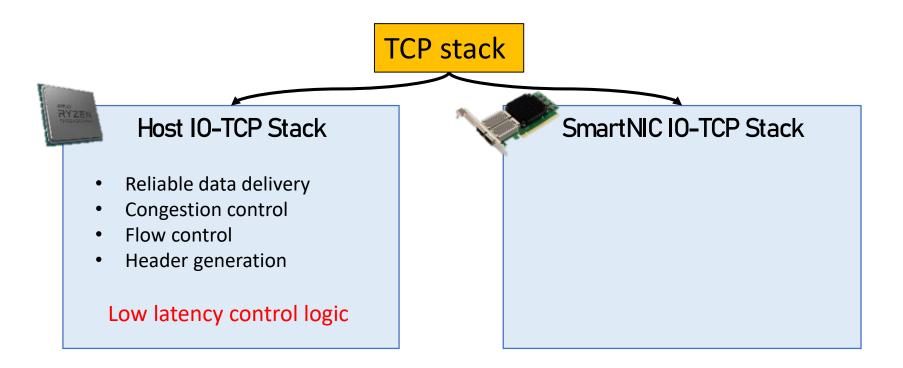
IO-TCP: Split TCP Stack Architecture for Content Delivery

Separation of TCP control/data planes



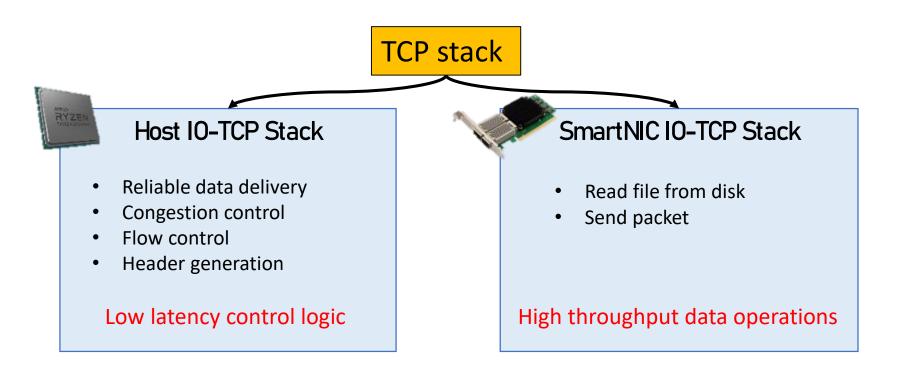
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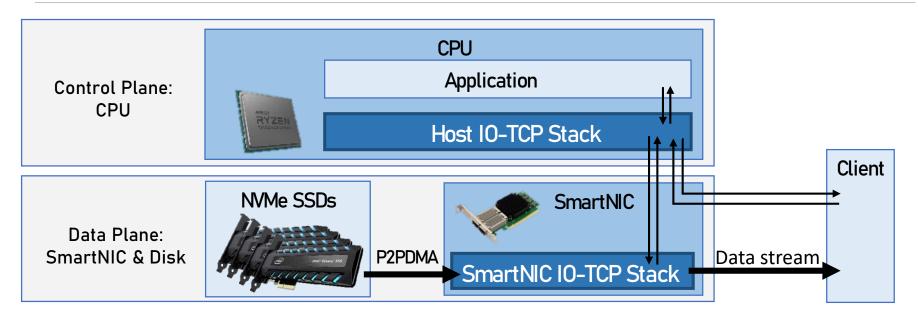


IO-TCP: Split TCP Stack Architecture for Content Delivery

Separation of TCP control/data planes

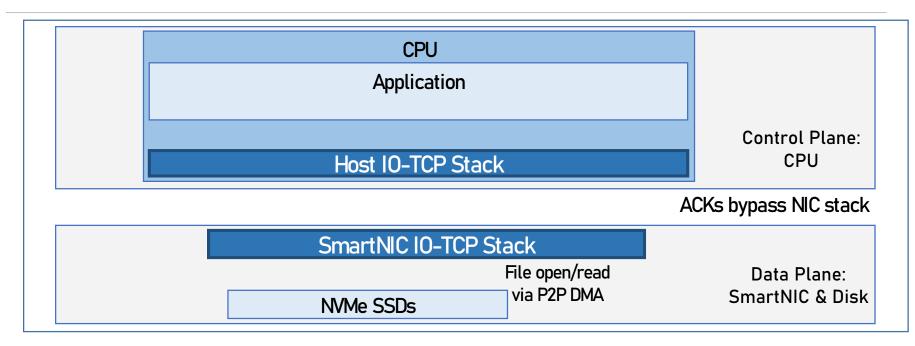


IO-TCP Overview

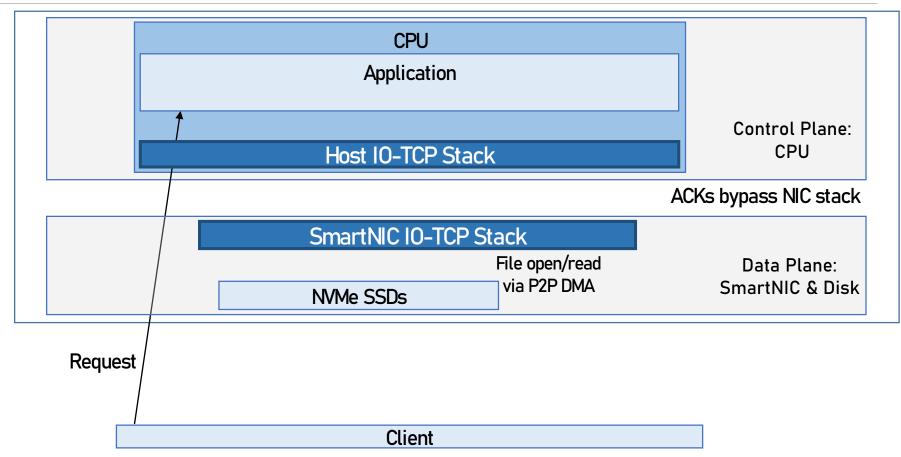


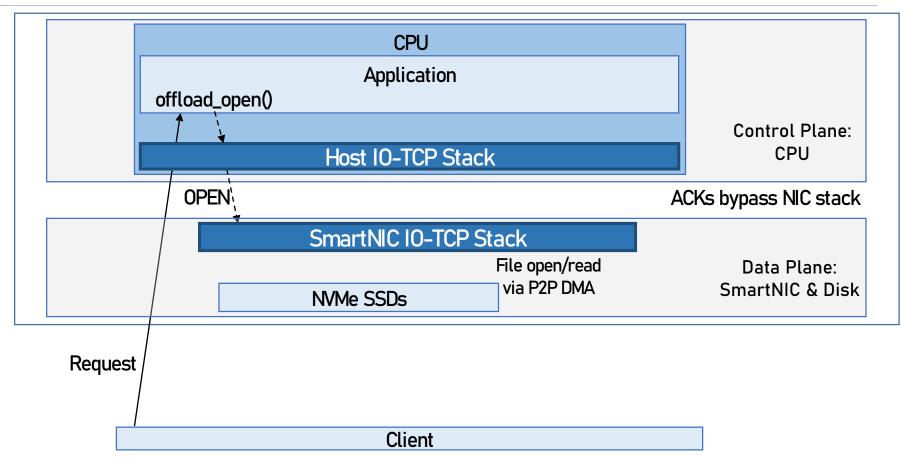
Provides 4 offload APIs for SmartNIC execution (offload_open(), offload_fstat(), offload_close(), offload_write())

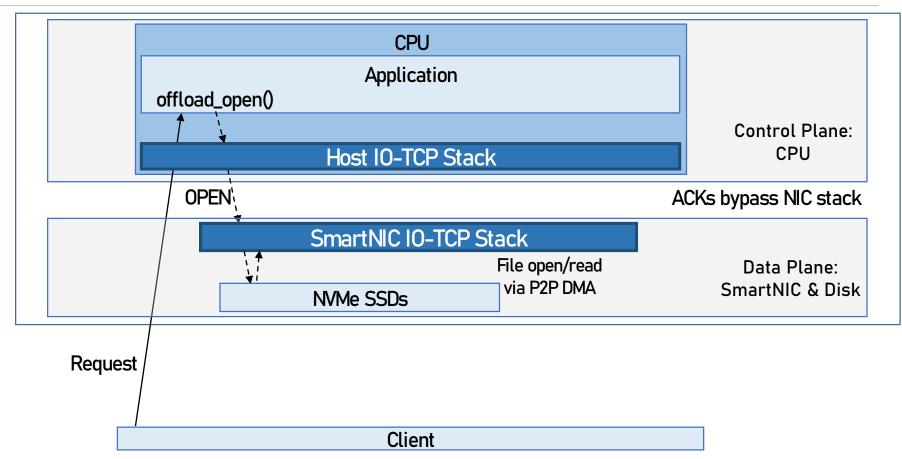
- 1. Application calls offload APIs for remote execution
- 2. Host sends a special command to SmartNIC for each API
- 3. SmartNIC stack performs corresponding IO operations

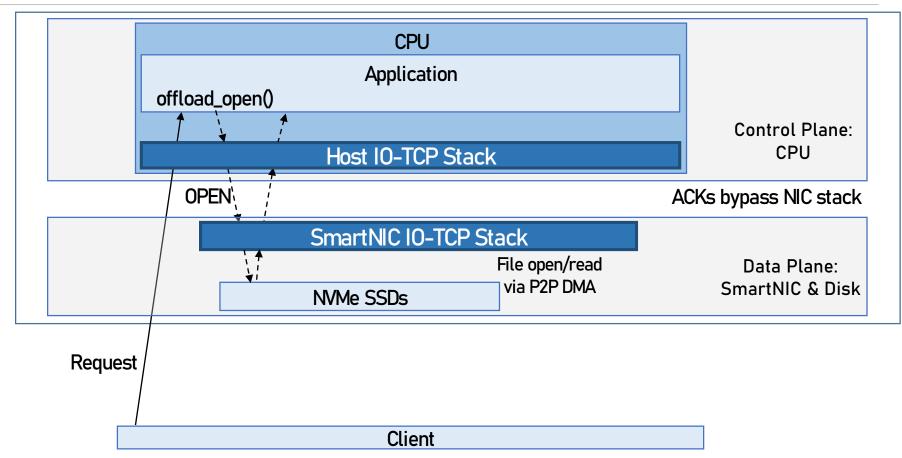


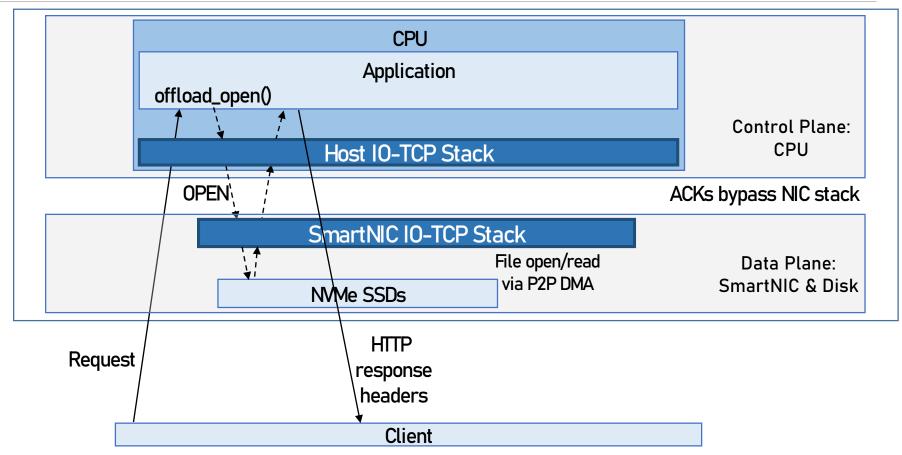
Client	
Stient	

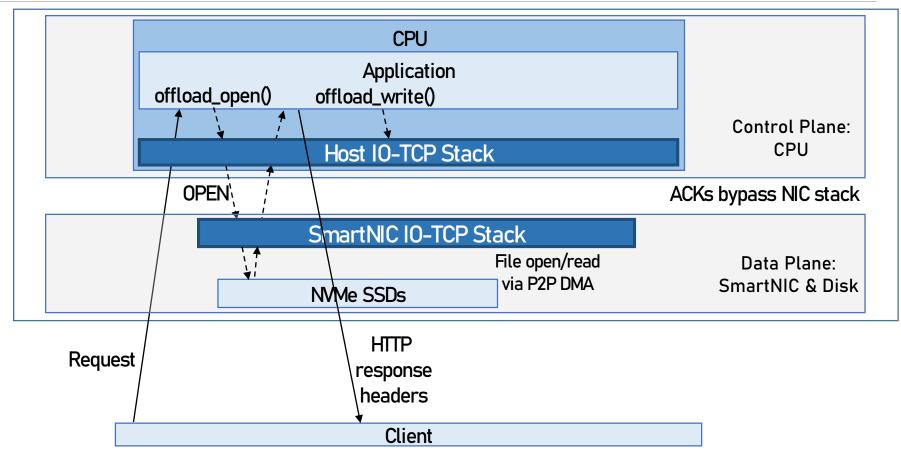


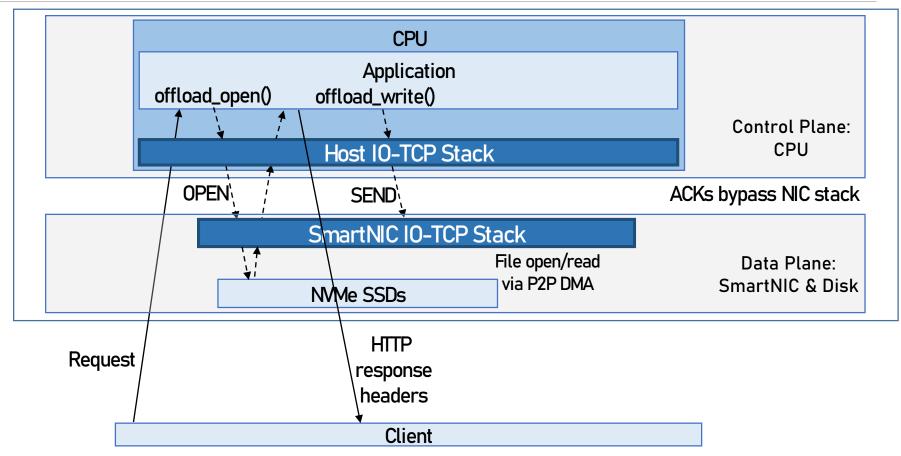


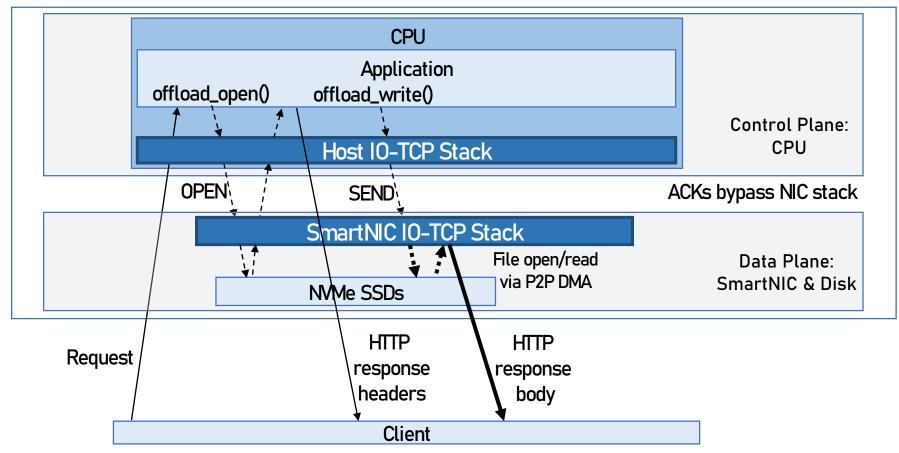


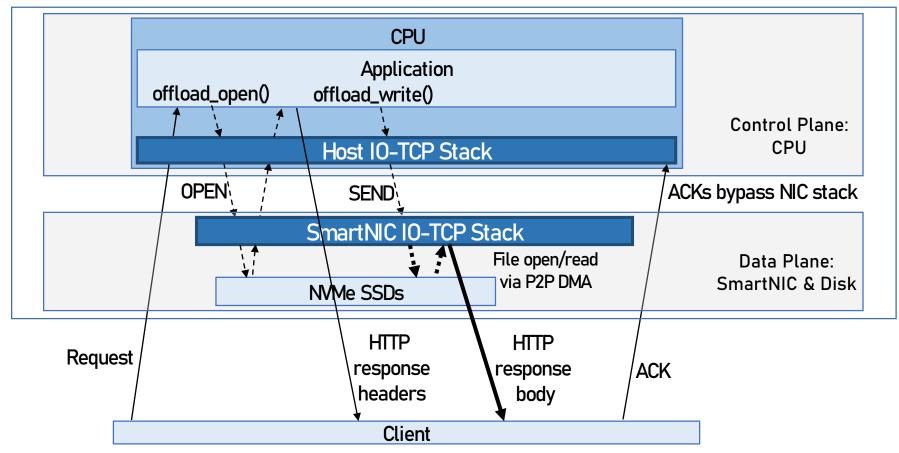


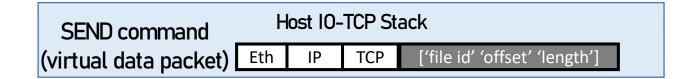


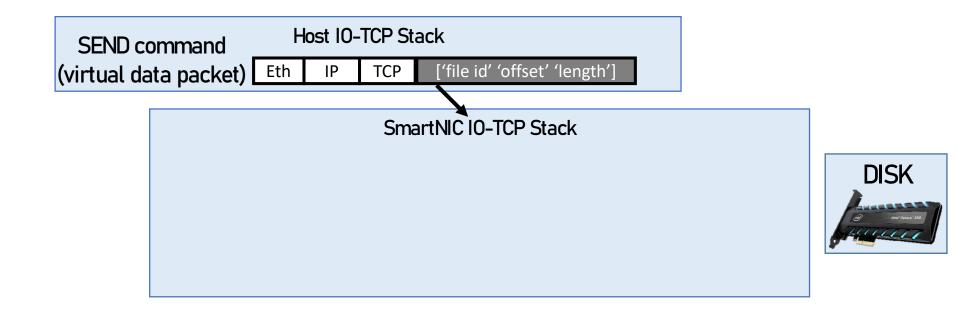


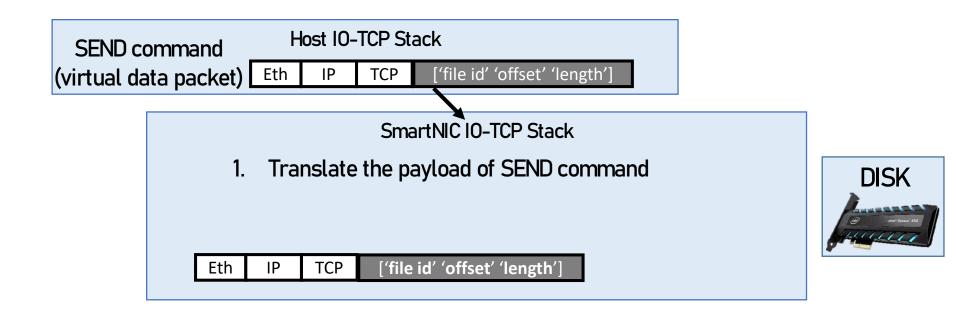


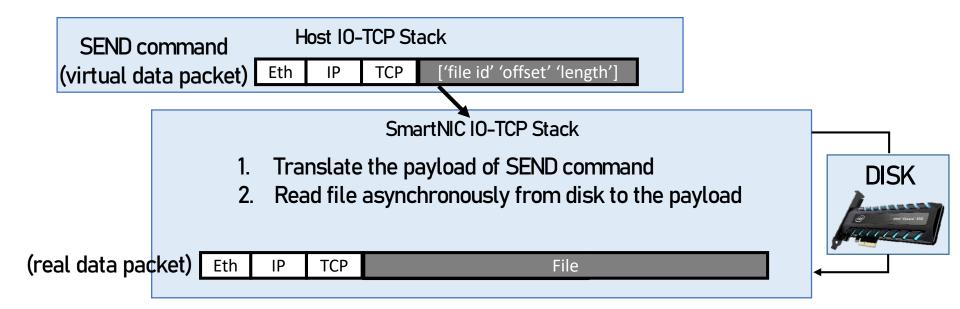


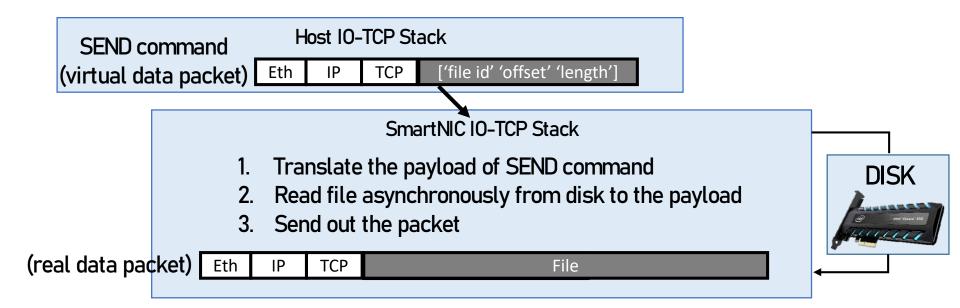


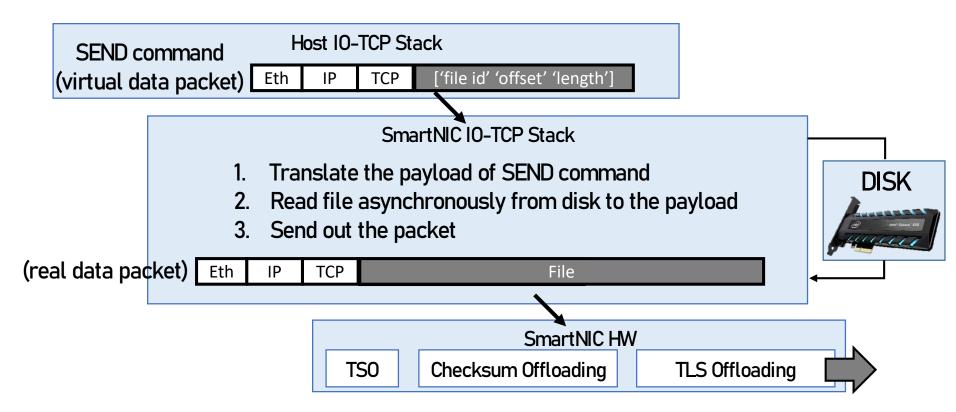












IO-TCP Challenges

Challenges

How to calculate accurate *packet RTT*?

How to deal with *retransmission*?

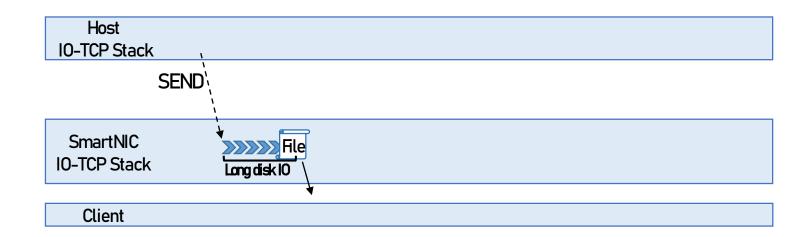
IO-TCP Challenges

Challenges

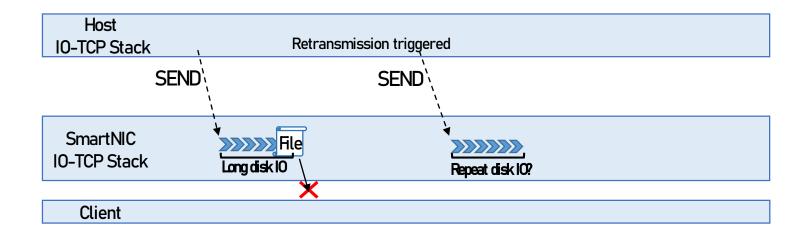
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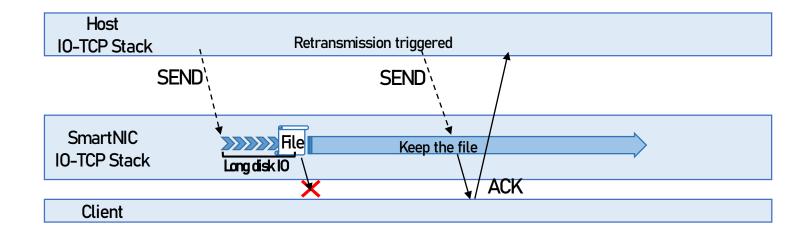
More details in the paper



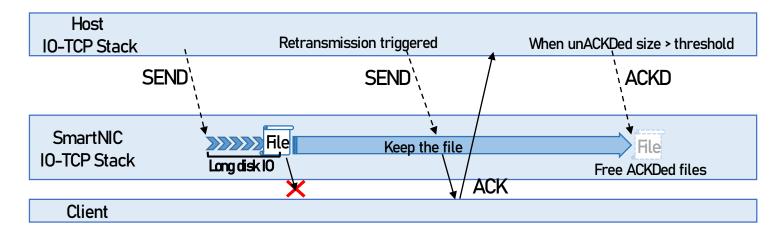
Re-reading the disk for retransmission could be slow!



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- Our approach
 - Keep the data on NIC memory until the data is confirmed to be delivered (ACK)
 - Problem: only Host receives all the ACKs (for control logic)



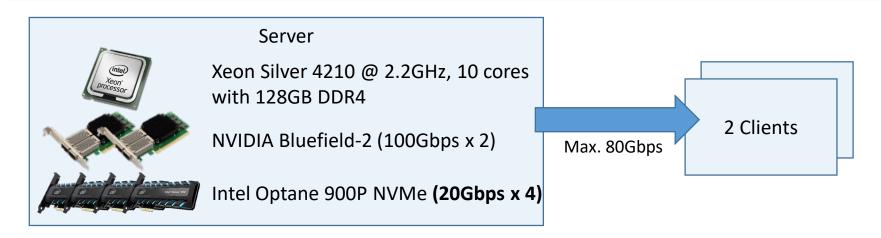
- Re-reading the disk for retransmission could be slow!
- Our approach
 - Keep the data on NIC memory until the data is confirmed to be delivered (ACK)
 - Problem: only Host receives all the ACKs (for control logic)
 - Solution: periodic notification of ACKnowledgeD(ACKD) sequence numbers (Host \rightarrow NIC)
 - Required memory size <= window size (e.g., 375MB for 100Gbps NIC with 30ms of average RTT)



Implementation

- Host stack: extended mTCP to support NIC offload
 - 1,793 lines of code modification on mTCP
- NIC stack: based on NVIDIA Bluefield2 SmartNIC
 - 1,853 lines of C code
 - We implement TSO, scatter-gather IO, and TLS crypto offload
- Easy to port existing apps
 - open(), fstat() and close() \rightarrow offload_open(), offload_fstat() and offload_close()
 - write() \rightarrow offload_write()
 - Porting Lighttpd server to IO-TCP: ~10 lines of code modification

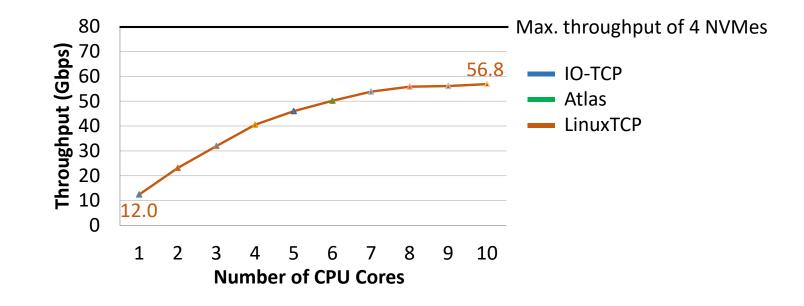
Experiment Setup



- Baselines
 - Lighttpd on Linux TCP with sendfile() (Kernel version: 4.14)
 - Atlas: webserver on kernel-bypass TCP stack with raw disk access [1]
 - Buffer-cache-free design
 - FreeBSD 1.10 & Chelsio 100Gbps NIC

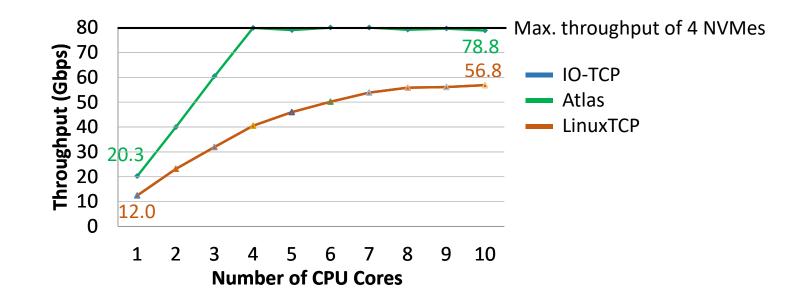
IO-TCP Performance – Plaintext

- 500KB video file chunks (disk bound)
- Lighttpd ported to IO-TCP



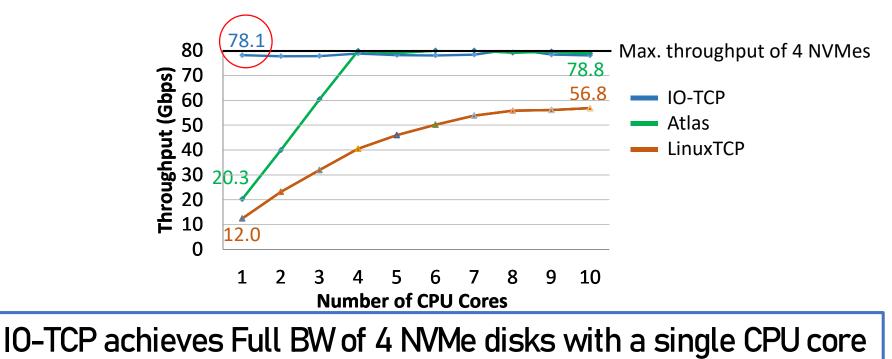
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IO-TCP Performance – Plaintext

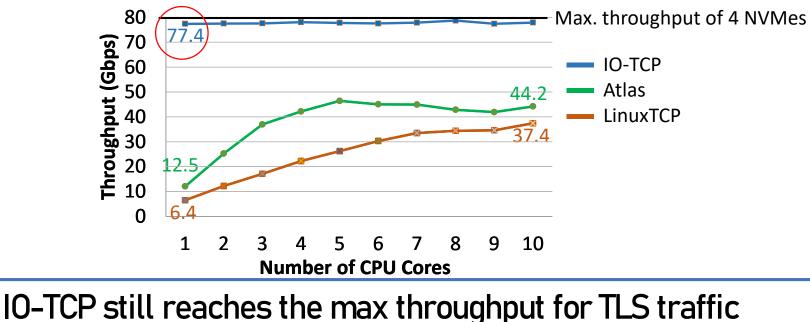
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- Lighttpd ported to IO-TCP



[1] "Disk|Crypt|Net: rethinking the stack for high-performance video streaming." SIGCOMM, 2017.

IO-TCP Performance – TLS

- 500KB video file chunks (disk bound)
- Lighttpd ported to IO-TCP
- Cipher mode: AES-GCM 256



[1] "Disk|Crypt|Net: rethinking the stack for high-performance video streaming." SIGCOMM, 2017.

Source of Performance Improvement

Separation of Control plane / Data plane

- No main memory read/write for IO
 - No CPU cache eviction by DDIO

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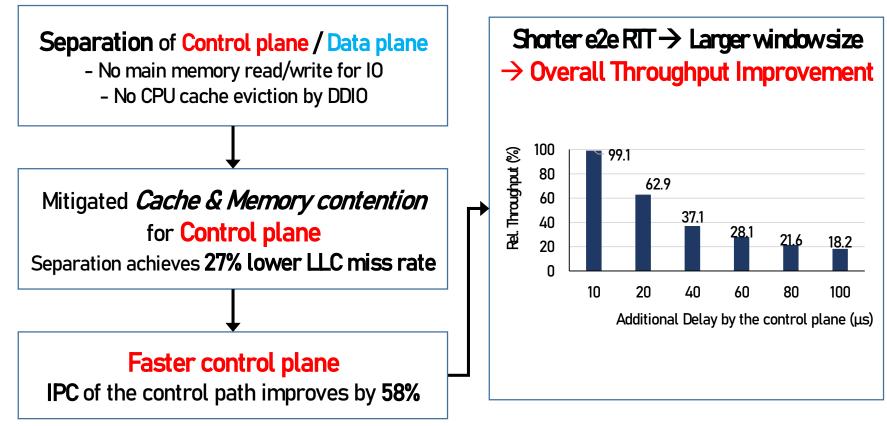
No main memory read/write for IO
No CPU cache eviction by DDIO

Mitigated *Cache & Memory contention* for Control plane Separation achieves 27% lower LLC miss rate

Faster control plane

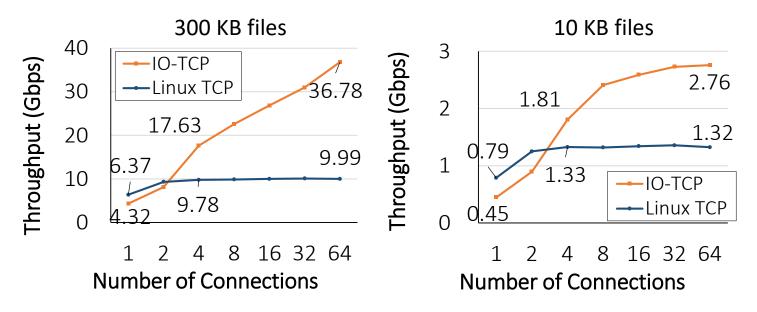
IPC of the control path improves by 58%

Source of Performance Improvement



IO-TCP Overhead Evaluation

- Overhead factors
 - Host-NIC communication overhead
 - Performance limit of Arm-based subsystem on BF2
 - > The fewer connections would be advantageous to CPU-only approach (Linux TCP)

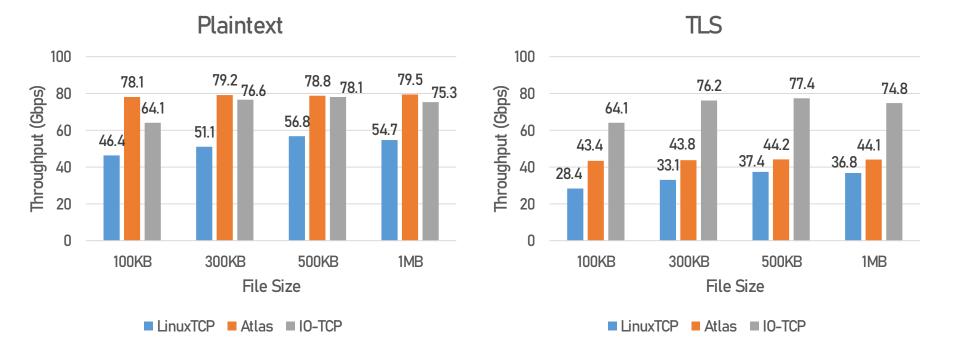




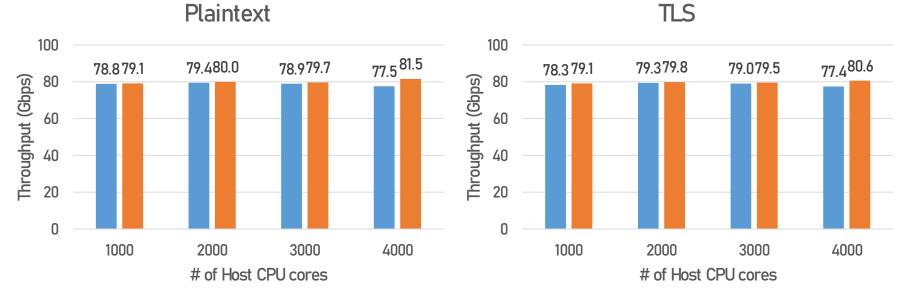
- BIG Trend: IO device advancement outpaces the rate of CPU capacity growth
- IO-TCP: a split TCP stack architecture for a content delivery system
 - CPU host stack carries out the control plane functionalities of a TCP stack
 - NIC stack serves as data plane of a TCP stack
- IO-TCP achieves full bandwidth of 4 NVMe disks with a single CPU core
 - Current bottleneck lies in SmartNIC memory bandwidth
 - SmartNIC with higher memory BW will improve the throughput even more
 - Bluefield-3 will achieve 140Gbps per NIC
- QUIC-based CDN can adopt our separated stack design as well

Thank you!

IO-TCP Performance – Varying File Sizes



IO-TCP Performance – Varying Number of Connections



1 2

■1 ■2

Linux TCP vs. IO-TCP

Lighttpd setup	Throughput (Gbps)
Linux TCP on Bluefield-2 only	11.98
Linux TCP on Bluefiend-2 and 1 CPU core	22.02
IO-TCP on Bluefield-2 and 1 CPU core	44.13

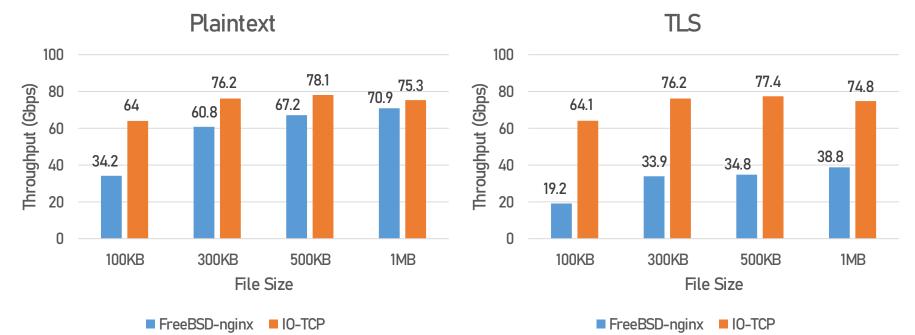
TCP Fairness

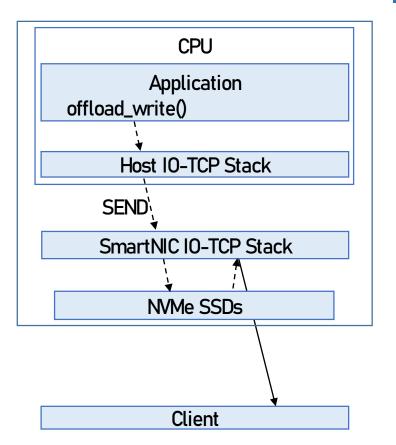
- Jain's fairness index with varying number of connections
 - IO-TCP: 0.91~0.97
 - Linux TCP: 0.90~0.97

User-level TCP Stacks vs. IO-TCP

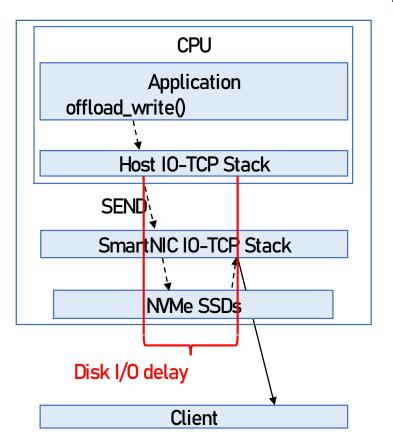
- Throughput with 500KB file delivery
 - TAS: 9.0 Gbps
 - mTCP: 21.4 Gbps
 - F-Stack: 36.0 Gbps
 - Linux TCP: 56.8 Gbps
- These stacks are not optimized for large-file content delivery
 - Optimized for small messages
 - Lacks of an implementation for sendfile() and a support for TSO

Asynchronous sendfile() on FreeBSD vs. IO-TCP

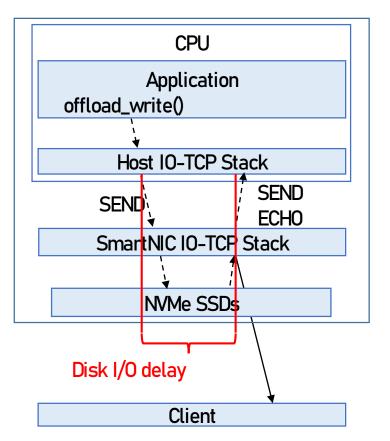




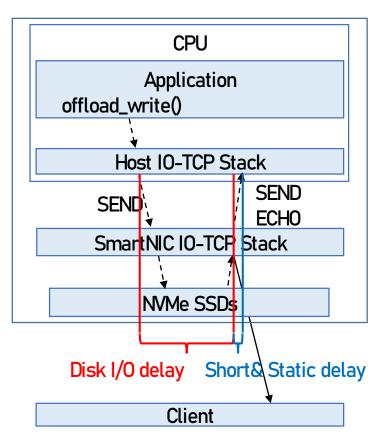
Retransmission timer at the host stack



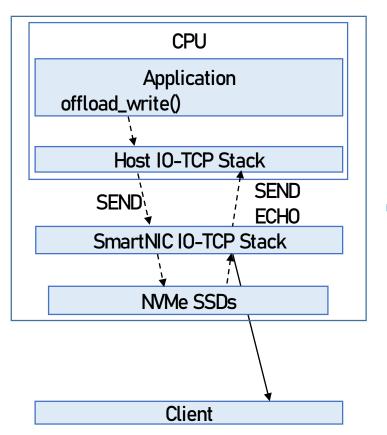
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 - Problem: disk access delay is added
 - Up to a few ms when backlogged



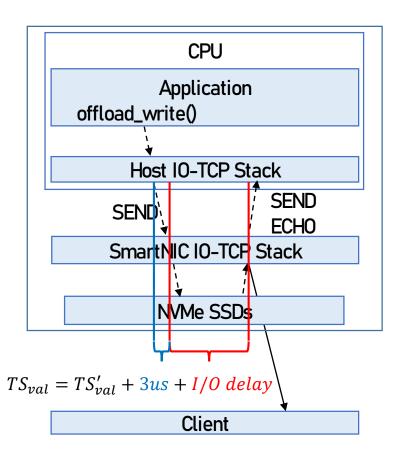
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 - Negligible overhead: a SEND for 10s~100s MTU
- RTT measurement with Timestamp option
 - Add the static delay(3us) and the I/O delay to the TS_{val}

CPU as a Bottleneck for NVMe

- With <u>multiple NVMe disks</u>, CPU can be a bottleneck
 - Intel Xeon Silver 4210 (2.20GHz)
 - 6x Intel Optane 900P
 - Simple fio
 - A single CPU core cannot even support **2 disks for 4K BS**

